

Fig. 1

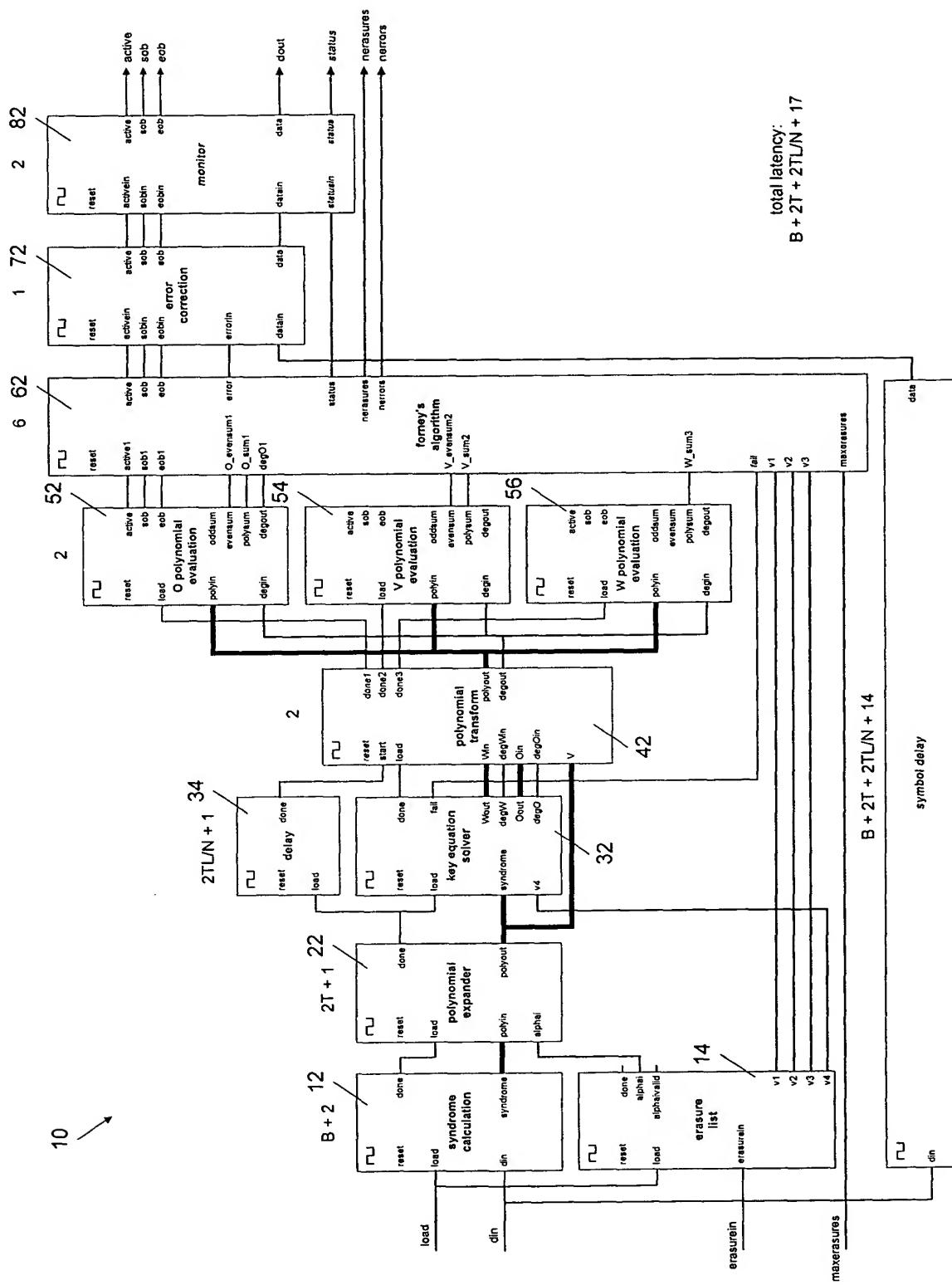
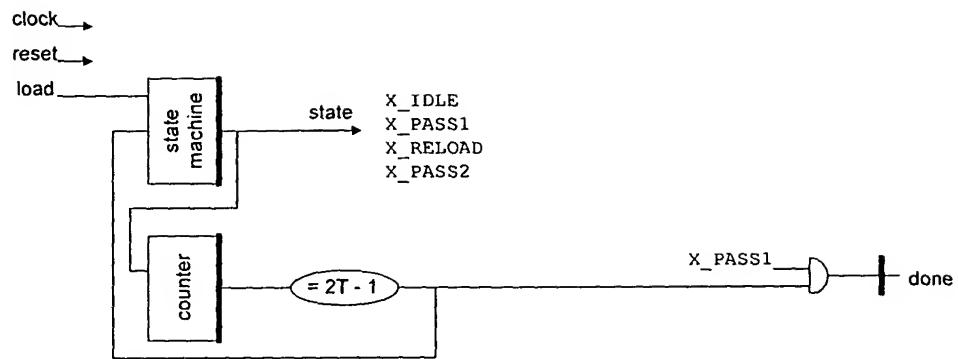
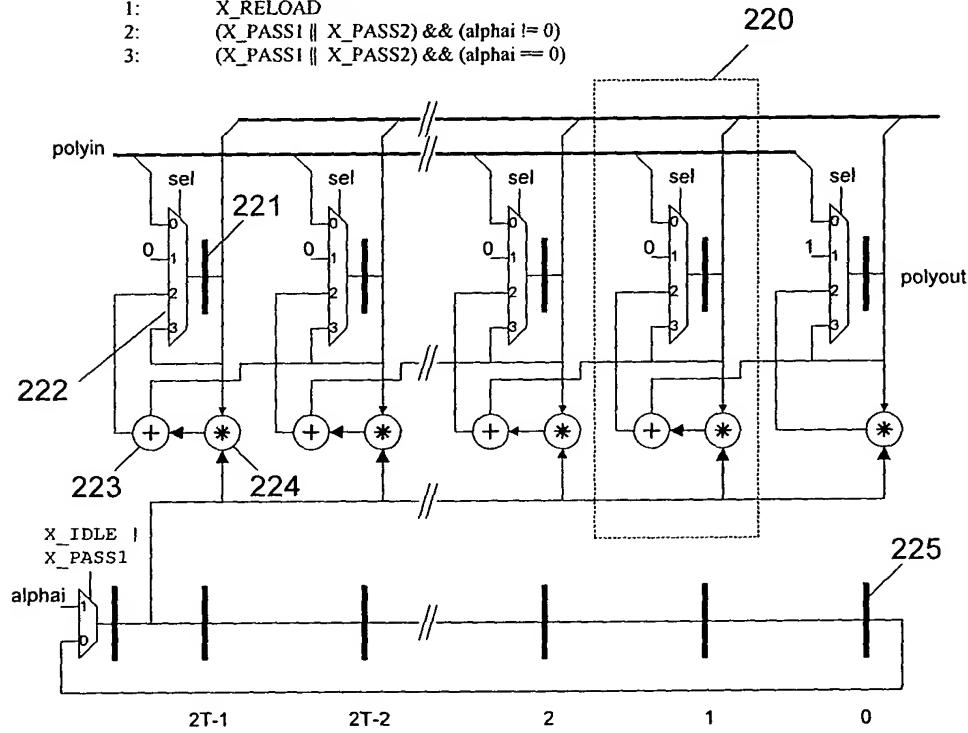


Fig. 2



sel: condition:
 0: X_IDLE && load
 1: X_RELOAD
 2: (X_PASS1 || X_PASS2) && (alphai != 0)
 3: (X_PASS1 || X_PASS2) && (alphai == 0)



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Fig. 3

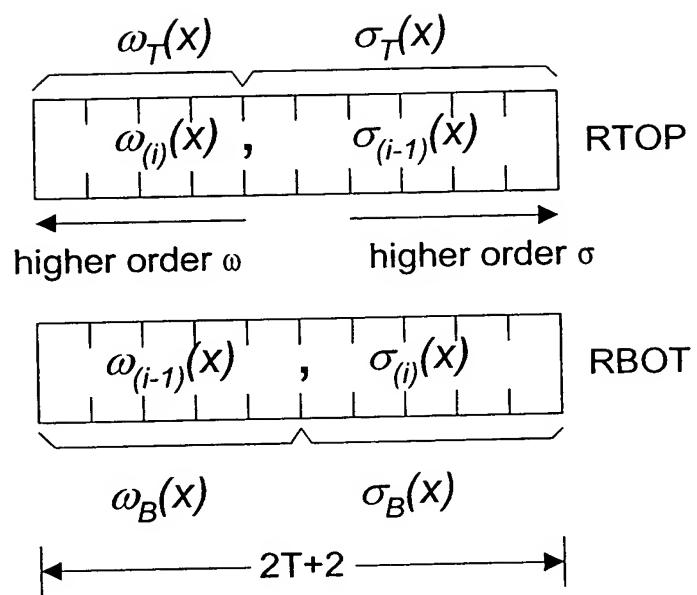
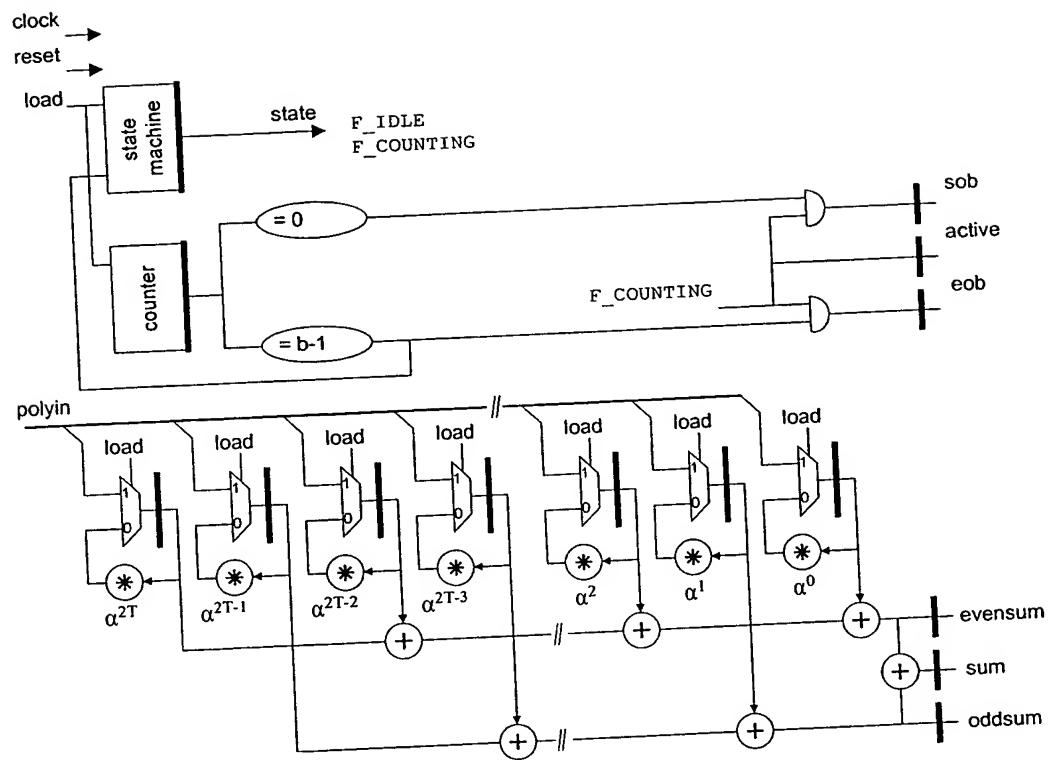


Fig. 4



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Fig. 5

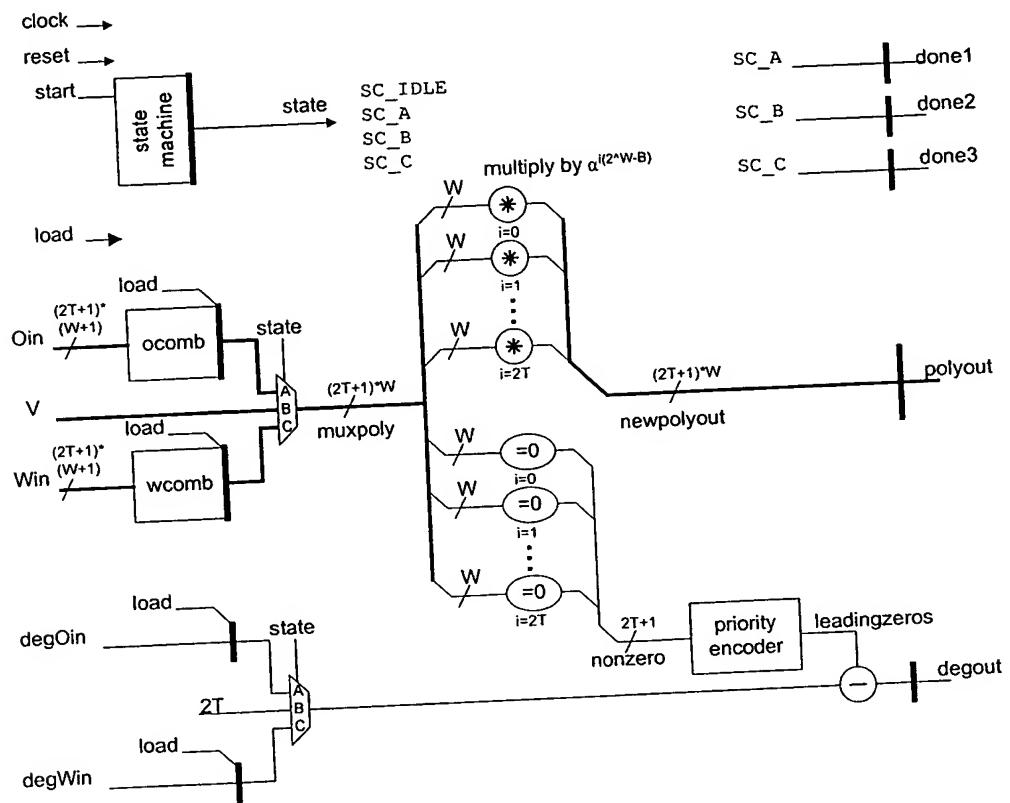


Fig. 6

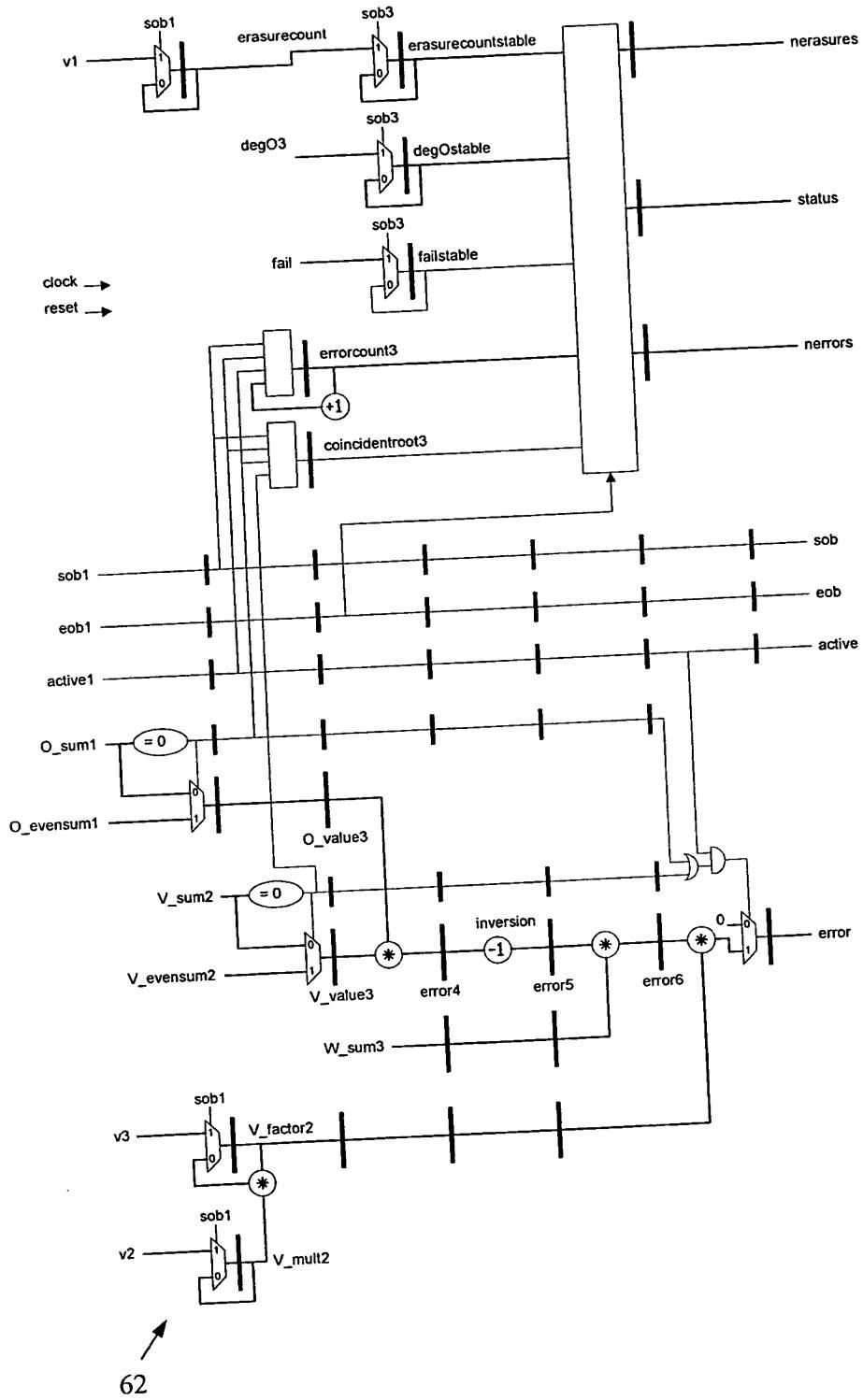


Fig. 7